INTEFERENCE

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	1	(monitoring adj2 responsive adj2 infrastructure).clm.	US-PGPUB	OR	ON	2006/11/30 13:42
L3	37	(simulation adj mode).clm.	US-PGPUB	OR	ON	2006/11/30 13:44
L4	1	(simulation and (test adj value) and (verify\$3 adj3 response)).clm.	US-PGPUB	OR	ON	2006/11/30 13:45
L5	1	(simulation and (Intelligent adj Platform adj Management)).clm.	US-PGPUB	OR	ON	2006/11/30 13:45
L6	69	((Intelligent adj Platform adj Management)).clm.	US-PGPUB	OR	ON	2006/11/30 13:45

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1575	(test\$3 adj3 (sensor monitor)) same compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 11:15
S2 /	101	714/41.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 11:01
S3	207	714/27.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/17 10:56
S5	488	714/37.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 10:57
S6	1088	714/47.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 10:57
S7	4	714/41.ccls. and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 16:32
S8	198	((test\$3 verify\$3) adj3 (sensor monitor) adj3 (operational functional correct\$2))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 10:59
S11		simulation adj mode and Intelligent adj Platform adj Management	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 11:03

S12	. 244	Intelligent adj Platform adj Management	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 11:03
S13	32	Intelligent adj Platform adj Management adj Interface adj Specification	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:07
S14		(test\$3 with (faulty adj (sensor monitor))) same compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 11:16
S15	1218	((error faulty fault) adj3 sensor) same compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:34
S17	24	"714"/\$.ccls. and S15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:34
S22	13605	((error faulty fault) adj3 sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 13:36
S25	_ 219	Intelligent adj Platform adj Management adj Interface	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 16:42
S26	0	S25 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:00

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S27	717	Platform adj management	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:01
S28		S27 and S22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:00
S29	161	Platform adj management and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:01
S30	715	((error faulty fault) adj3 sensor) and simulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 14:07
S31	85	((error faulty fault) adj3 sensor) same simulation	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 15:54
S32	148	"714"/\$.ccls. and ((error faulty fault) adj3 sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 15:28
S34	12354	(test\$3 adj3 sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 15:55
S35	. 447	(test\$3 adj3 sensor) with (error fault failure)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 15:56

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S36	520	test adj mode with sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 18:09
S37	80	(test adj mode) adj3 sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/17 16:33
S38	. 118	(Intelligent adj Platform adj Management adj Interface) and sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 18:08
S39	0	(Intelligent adj Platform adj Management adj Interface) and (faulty adj sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 16:44
S40	19	(Intelligent adj Platform adj Management adj Interface) and ((fault failure error) with sensor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/17 18:10
S41	219	(Intelligent adj Platform adj Management adj Interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR T	ON	2006/07/19 08:08
S42	17915	sensor adj3 (fault failure error)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 18:09
S43	6349	sensor adj (fault failure error)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON .	2006/07/17 18:10

S44	0	(Intelligent adj Platform adj Management adj Interface) and (sensor adj (fault failure error))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:06
S45	93	"714"/\$.ccls. and sensor adj (fault failure error)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/17 18:10
S46	1677	(simulat\$3 emulat\$3) adj3 sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:15
S47	15	"714"/\$.ccls. and (simulat\$3 emulat\$3) adj3 sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:26
S48	4	(Intelligent adj Platform adj Management adj Interface) and (simulat\$3 emulat\$3) adj3 sensor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:40
S50	836	(simulat\$3 emulat\$3) adj3 sensor and (error fault failure)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:28
S51	159	((simulat\$3 emulat\$3) adj3 sensor) same(error fault failure)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 08:28
S54		(Intelligent adj Platform adj Management adj Interface) and sensor and ((test\$3 debug\$4 diagnostic) adj3 (controller ASIC microcontroller CPU monitor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/19 10:08

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S55	105	((simulat\$3 emulat\$3) adj3 sensor) and ((test\$3 debug\$4 diagnostic) adj3 (controller ASIC microcontroller CPU monitor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 09:08
S56	192	714/28.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 09:09
S58	12	S56 and sensor	US-PGPÜB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 09:10
S59	7	"6480809"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 09:59
S60	916	(plurality with sensors) and ((test\$3 debug\$4 diagnostic) adj3 (controller ASIC microcontroller CPU))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 10:00
S61		"714"/\$.ccls. and (plurality with sensors) and ((test\$3 debug\$4 diagnostic) adj3 (controller ASIC microcontroller CPU))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 10:00
S62	12	(Intelligent adj Platform adj Management adj Interface) and ((test\$3 debug\$4 diagnostic) adj3 (controller ASIC microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON ··	2006/07/19 10:15
S63	288	((test\$3 debug\$4 diagnostic) adj3 sensor adj3 (monitor\$3 controller ASIC microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 10:16

S64	2	((test\$3 debug\$4 diagnostic) adj3 sensor adj3 monitor\$3 adj3 (module controller ASIC microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 10:16
S65	221	((test\$3 debug\$4 diagnostic) adj3 sensor adj3 (monitor\$3 controller ASIC microcontroller module)) and compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:25
S66		((test\$3 debug\$4 diagnostic) adj3 sensor adj3 (monitor\$3 controller ASIC microcontroller module)) and (compar\$3 adj3 (value output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 11:04
S67	96	((simulat\$3 emulat\$3) adj3 sensor adj3 output)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 10:23
S68	48	((simulat\$3 emulat\$3) adj3 sensor adj3 output) and (test\$3 debug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/19 13:16
S69	35	((simulat\$3 emulat\$3) adj3 sensor adj3 output) and (test\$3 debug\$4) and compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 11:03
S70	7302	((test\$3 debug\$4 diagnostic) adj3 (monitor\$3 controller ASIC microcontroller module)) and (compar\$3 adj3 (value output))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:10
S71	6085	((test adj pattern) (simulat\$3 with input)) with (microcontroller computer cpu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:15

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S72	3818	S71 and (test\$3 debug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:15
S73	4224	((simulat\$3 emulat\$3) adj3 output) and (test\$3 debug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:16
\$76	1751	"714"/\$.ccls. and (test adj (value input)) same (test\$3 debug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:32
S77	192	714/28.ccis.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:33
S78	15	"714"/28.ccls. and (test adj (value input)) same (test\$3 debug\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:36
S79	3	"714"/28.ccls. and (test adj (value input)) same ((test\$3 debug\$4) adj mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 13:42
S86	44	"714"/\$.ccls. and ((bit value) adj6 indicat\$3 adj6 test\$3) same table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 17:01
S87	1	"714"/\$.ccls. and (indicat\$3 adj (test adj mode)) same table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 17:02

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S88	35	(indicat\$3 adj3 (test adj mode)) same table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/20 10:26
589	. 1340	table adj3 indicat\$3 adj3 test\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR ·	ON	2006/07/19 17:10
S90	33	"714"/\$.ccls. and (table adj3 indicat\$3 adj3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/19 17:10
S91	106	(status adj table) and (indicat\$3 adj3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR .	ON	2006/07/20 10:48
S92	41	simulat\$3 and (status adj table) and (indicat\$3 adj3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/20 10:42
S93 _.	3	(status adj table) with (indicat\$3 adj3 test\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/07/20 10:48
S94	278	PCB and (test adj mode) and table	USPAT	OR	OFF	2006/07/20 10:58
S95	3	PCB and (table with indicat\$3 with (testing (test adj mode)))	USPAT	OR	OFF	2006/07/20 10:56
S96	278	PCB and (test adj mode) and table	USPAT	OR	OFF	2006/07/20 10:57
S97	15	PCB and (test adj mode) and (table with testing)	USPAT	OR	OFF	2006/07/20 10:59
S98	486	(simulation adj mode)	USPAT	OR	OFF	2006/07/20 11:03
S99	144	(simulation adj mode) and sensor and table	USPAT	OR	OFF	2006/07/20 11:03
S10 0	6	(table with (simulation adj mode)) and sensor	USPAT	OR	OFF	2006/07/20 11:04

S10 1	70	(set\$4 adj5 table) same (current\$3 adj3 (test\$3 simulat\$3))	USPAT	OR	OFF	2006/07/20 11:12
S10 2	0	(set\$4 adj5 table) same (current\$3 adj3 (test\$3 simulat\$3) with run\$4)	USPAT	OR	OFF	2006/07/20 11:13
S10 4	750	(set\$4 adj5 table) same ((test\$3 simulat\$3) adj4 run\$4)	USPAT	OR	ON	2006/07/20 11:14
S10 5	1	"714"/\$.ccls. and (set\$4 adj5 table) same ((test\$3 simulat\$3) adj4 run\$4)	USPAT	OR	ON	2006/07/20 11:15
S10 6	151	"714"/\$.ccls. and (table) same ((test\$3 simulat\$3) adj4 run\$4)	USPAT	OR	ON	2006/07/20 11:15
S10 7	5	"714"/\$.ccls. and (table) same ((test\$3 simulat\$3) adj3 current\$3 adj4 run\$4)	USPAT	OR	ON	2006/07/20 11:16
S10 8	8	(table) same ((test\$3 simulat\$3) adj3 current\$3 adj4 run\$4)	USPAT	OR	ON	2006/07/20 11:17
S11 0	133	(table) with indicat\$3 with ((test\$3 simulat\$3) adj3 (current\$3 run\$4))	USPAT	OR	ON	2006/07/20 13:29
S11 6	1675	714/27,28,37,41,47.ccls.	USPAT	OR	OFF	2006/11/30 11:44
S11 7	954078	@pd>"20060602"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 11:44
S11 8	197	S117 and ((simulat\$3 emulat\$3) adj mode)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/11/30 13:33
S11 9	120	S116 and S117	USPAT	OR	OFF	2006/11/30 13:03
S12 0	. 54	(Intelligent adj Platform adj Management adj Interface) and S117	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:06
S12 1	34	(test\$3 debug\$4 monitor\$3) with (Intelligent adj Platform adj Management adj Interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:24

S12 2	1	(verify\$3) with (Intelligent adj Platform adj Management adj Interface)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:24
S12 3	33	S117 and ((test\$3 debug\$4 diagnostic) adj3 sensor adj3 (monitor\$3 controller ASIC microcontroller module))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/30 13:25